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If there are any questions regarding this matter, please contact me at the telephone number provided below.

Respectfully submitted,



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5957-05500
PATENT

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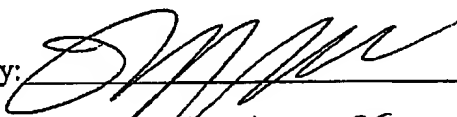
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<u>Filing Number</u>	<u>Country Code</u>	<u>Filing Date</u>	<u>Page 1 of 7</u> <u>Title / Inventors</u> <u>(US Patent)</u>
699,600	US	14/05/1991	Method for manufacturing an active matrix display screen with storage capacitors / Morin, Lo, Comelleo
264,199	US	22/06/1991	Active matrix display screen with storage capacitors formed of conductive blocks, semiconductive material, nonconductive material and capacitive lines / Morin, Lo, Comelleo
792,262	US	15/11/1991	Method for creating integrated circuit layers of a fixed depth and corresponding integrated circuit / Hiron
91403055/6	DE	14/11/1991	
90143061	FR	16/11/1990	
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764,211	US	23/09/1991	Bratechvergent for a control circuit in a thin film transistor display screen / Seng, J. L. M. Marol
764,196	US	26/09/1991	Sample and hold circuit for a thin film transistor display screen / Seng, J. L. M. Marol
886,243	US	18/02/1992	Collection of a thin film transistor compatible with MOS technology / Moullin, Boie
92420047/0	EP	27/02/1992	
9101984	FR	13/02/1991	
832,078	US	06/02/1992	Process for forming a localized oxidation on a silicon substrate and corresponding integrated circuit / Stupnik, Bang, Muller, Boie
9101981	FR	07/02/1991	
866,342	US	10/04/1992	Process for the production of thin film transistors / Cheban
9101618	US	08/07/1992	Method for manufacturing a vertical field effect transistor / Cheban, Boie, Naudet
92401909/4	DE	03/07/1992	
92401909/4	GB	03/07/1992	
9108677	FR	10/07/1991	
911013	US	09/07/1992	Active matrix high definition liquid crystal display structure / Morin, Chouan, Vionze
92401987/0	GB	09/07/1992	
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<u>Filing Number</u>	<u>Country Code</u>	<u>Filing Date</u>	<u>Page 2 of 7</u> <u>Title / Inventors</u> <u>(US Patent)</u>
2311024	US	20/04/1994	Method of clock signal recovery and of synchronization for the reception of information elements transmitted by an ATM network and device for the implementation of the method / Rainard
944008531	DE	19/04/1994	
93104775	FR	22/04/1993	
944008531	GB	19/04/1994	
2281479	US	15/04/1994	Signal processing device using several different filtering, especially for audio frequency coding of voice signals / Balastro, Selin
944007806	DE	11/04/1994	
93104552	FR	16/04/1993	
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279546	US	25/07/1994	Optical amplifier having a doped fluoride glass optical fibre and process for producing this amplifier / Ronard, Guilbert, Hammed
944017060	GB	25/07/1994	
93109165	FR	26/07/1993	
944017060	DE	25/07/1994	
944017060	IT	25/07/1994	
99166	US	02/09/1994	Process for the transposition of an optical modulation of one wavelength to another adjustable wavelength / Dupont, Authier, Trombet
944019843	GB	07/09/1994	
944019843	DE	07/09/1994	
9310737	FR	09/09/1993	
935953	US	09/02/1995	Decreasing device, particularly for optical fibres / Cresnol, Galleani, Caudrelier
944016731	FR	11/02/1994	
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9374472	US	24/01/1995	Optical amplifier with a doped fluoride glass optical fibre and process for producing said amplifier / Semelkoff, Ronard, Guilbert
92591	US	20/06/1995	Electron memory addressing device especially for a memory organized into banks / Quilleyers, Dufal
954014445	DE	20/06/1995	
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5641120	US	30/03/1995	Electrically conductive polymer compositions, production process and coated substrates / (Crisse, Delabouglise, Ciprilli)
959152807	GB	30/03/1995	
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9408312	FR	31/03/1994	
5271137	US	12/09/1995	Sequential access asynchronous memory device and corresponding process for storage and reading / (Mays, Well)
954022133	DE	04/10/1995	
94112170	FR	12/10/1994	
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569319	US	08/12/1995	Electronic component capable, in particular, of performing a division of two numbers to the base 2 / (Duff, Robert)
9415507	FR	22/12/1994	
954023098	DE	13/12/1995	
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639088	US	24/04/1996	Device for programmable delay of an analog signal and corresponding acoustic antenna / (Le Rouneur, Balstro)
9640035110	FR/EP	22/04/1996	
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95049118	FR	25/04/1995	
684004620	US	21/02/1996	Minimizing program code storage for performing regular and repetitive operations in a programmable processor / (Duff, Rivas)
964003339	DE	19/02/1996	
95021311	FR	28/02/1995	
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674675	US	02/07/1996	Apparatus and a method for identifying and splitting multimode fibers / (Le Nonne, Kerin, Le Marec)
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984014100	EP	11/06/1998	
9707892	EP	20/06/1997	
09384362	US	26/08/1999	Instrument for measuring the near end cross talk spectrum length of multicore fibers / Boller, Bzapala, Cve
994020102	EP	23/08/1999	
9810768	EP	27/08/1998	
09289667	US	12/04/1999	Electronic method of signal processing, device for implementing the filter and use thereof
994008522	EP	08/04/1999	
10436509	JP	12/04/1999	
98104557	EP	10/04/1998	
09956487	US	07/03/2000	Process for testing integrated circuits with access to the memory points of the circuit / Barthe
9902822	FR	03/03/1999	
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99106425	FR	26/05/1999	
009296260	GB	18/05/2000	
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09511330	US	23/02/2000	High speed addressable reception device and corresponding multimedia receiver / Andre, Senn
99103769	FR	23/03/1999	
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09/553,916	US	20/04/2000	Integrated circuit device comprising an inductor with high quality coefficient / Merokel, Rons, Senn, Roumle
99/04986	FR	20/04/1999	
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10/019,170	US	23/06/2000	Method for compensating non-linearity on a sigma-delta analog-to-digital converter / Morche
99/08323	FR	29/06/1999	
09/518,244	US	06/03/2000	Radio frequency transmitter with a high degree of integration and possibly with self-calibrating image deletion / Andre
004600193	EP	02/03/2000	
99/05768	FR	23/03/1999	
09/569,524	US	28/04/2000	Frequency control system that stabilizes an output through both a dominant voltage controlled oscillator via sampling a generated clock into four states / Majors
004011003	EP	19/04/2000	
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09/503,448	US	16/02/2000	Process for the anisotropic etching of an organic dielectric polymer material by an plasma stand application in microelectronics / Dolberg, Ruard
99/01925	FR	17/02/1999	
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10/018,680	US	08/06/2000	Method for making a silicon substrate comprising a buried thin silicon oxide film / Jurozak, Skolnik
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09/620896	US	21/07/2000	Method for creating topographical features on a micro-electronic substrate // Salmiz, Paolo, Schiavone, Italia
219525	JP	19/07/2000	
2000-0042258	KR	22/07/2000	
99/09521	FR	22/07/1999	
00402063.3	GB	19/07/2000	
00402063.3	IT	19/07/2000	
10/1684041	US	08/06/2001	Low-noise spectroscopic ellipsometer // Remond
0007426	FR	09/06/2000	
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2002-502401	JP	08/06/2001	
09/912057	US	25/07/2001	Resist a double resist layer for extreme ultraviolet (EUV) photolithography and an extreme ultraviolet light (EUV) photolithography process // Salmiz
01401972.3	EP	23/07/2001	
00109769	FR	26/07/2000	
10/380786	US	8/09/2001	Device for magnetic measurement of radiation frequency magnetic field with constant amplitude and frequency // Baryls, Geoffroy
000119281	FR	19/09/2000	
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10/382582	US	10/07/2001	Substrate absorption structure, method for regenerating optical components and method for making same // Marceau, Remond, La Courte, Delafosse
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0103829	FR	21/03/2001	Method and device for transmuting a video sequence comprising a face, especially in a mobile videophone system // Roux, Fern
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<u>Filing Number</u>	<u>Country Code</u>	<u>Filing Date</u>	<u>Page 7 of 7</u> <u>Title / Inventors</u> <u>(US Patent)</u>
10/725,978	US	24/05/2002	Large capacity automatic distribution method for on the fibres, development method for automatic connection/ disconnection of the binding fibres / Morel, Jean-François Bouche
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01/070,321	FR	29/03/2001	
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2003-600628	JP	24/05/2002	

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on December 3rd, 2004

ASSIGNOR

By: France Telecom

Name: François JAMET

Title: Directeur de la Propriété Industrielle et de la Valorisation

(Signature MUST be notarized)

Je soussigné Eric de La HAYE SAINT HILAIRE
Notaire à Paris, Certifie véritable
la signature de M. François JAMET
apposée ci-contre
Paris, le 3 décembre 2004

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